

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6089	(("clock and data recovery") or CDR)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/04 14:12
L4	1173	(((clock adj2 data adj2 recovery) or CDR) adj circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 14:55
L5	543	(((clock adj2 data adj2 recovery) or CDR) adj circuit\$1) and (phase near2 (generat\$4 or detect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 14:57
L6	294	(((clock adj2 data adj2 recovery) or CDR) adj circuit\$1) and (phase near2 (generat\$4 or detect\$4)) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 14:58
L7	295	(((clock adj2 data adj2 recovery) or CDR) adj circuit\$1) and (phase\$1 near2 (generat\$4 or detect\$4)) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 15:17
L8	290	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj circuit\$1) and ((phase\$1 or edge) near2 (generat\$4 or detect\$4)) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 16:32
L9	13	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj circuit\$1) and ((phase\$1 or edge) near2 (generat\$4 or detect\$4)) and jitter and NRZI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 15:48
L10	0	vining-suzanne.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 15:49

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L13	241	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) and ((phase\$1 or edge) adj1 (generat\$4 or detect\$4)) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 16:33
L14	209	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) same ((phase\$1 or edge) adj1 (generat\$4 or detect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 16:34
L15	88	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) same ((phase\$1 or edge) adj1 (generat\$4 or detect\$4))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/04 16:35
L16	48	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) same ((phase\$1 or edge) adj1 (generat\$4 or detect\$4)) and jitter	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/04 16:36
L17	67	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) and ((phase\$1 or edge) adj1 (generat\$4 or detect\$4)) and jitter and (phase adj2 clock\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/04 16:36
L18	37	(((clock adj1 data adj1 recovery) or CDR or (clock/data adj1 recovery)) adj1 circuit\$1) and ((phase\$1 or edge) adj1 (generat\$4 or detect\$4)) and jitter and (phase adj1 clock\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/04 16:36